

Abstract of the Disclosure

A circuit and method of generating delayed tap signals can adjust a delay difference by interpolating two input clock signals as indicated by an offset information signal. In the circuit, first and second tap signals are generated by interpolating first and second clock signals in response to the offset information. A delay difference between output tap signals is adjusted by an amount indicated by the offset information. Thus, tap signals having a fine delay difference can be obtained by adjusting the offset information.